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Term	Documents
(14 AND 6).PGPB,USPT.	2
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side by side			
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L26</u>	16 and 114	2	<u>L26</u>
<u>L25</u>	16 and 113	11	<u>L25</u>
<u>L24</u>	16 and 112	4	<u>L24</u>
<u>L23</u>	16 and 111	3	<u>L23</u>
<u>L22</u>	16 and 110	6	<u>L22</u>
<u>L21</u>	16 and 19	36	<u>L21</u>
<u>L20</u>	15 and 114	3	<u>L20</u>
<u>L19</u>	15 and 113	12	<u>L19</u>
<u>L18</u>	15 and 112	6	<u>L18</u>
<u>L17</u>	15 and 111	3	<u>L17</u>
<u>L16</u>	15 and 110	6	<u>L16</u>

<u>L15</u>	15 and 19	37	<u>L15</u>
<u>L14</u>	(718/104-108 )![CCLS]	3446	<u>L14</u>
<u>L13</u>	(710/58-74)![CCLS]	4872	<u>L13</u>
<u>L12</u>	(709/236-244)[CCLS]	6224	<u>L12</u>
<u>L11</u>	(712/210-228)[CCLS]	5967	<u>L11</u>
<u>L10</u>	(712/2-22)![CCLS]	1971	<u>L10</u>
<u>L9</u>	(712/2-300)[CCLS]	13531	<u>L9</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L8</u>	L6 and (one or single) near2 chip	59	<u>L8</u>
<u>L7</u>	L6 and (simd or single near1 instruction)	4	<u>L7</u>
<u>L6</u>	L5 and bandwidth	248	<u>L6</u>
<u>L5</u>	(allocat\$5 or distribut\$5) near12 (packet\$1 or frame\$1) near5 (unpredict\$5 or variable or different) near15 (size or width or length)	498	<u>L5</u>
<u>L4</u>	(allocat\$5 or distribut\$5) near12 (packet\$1 or frame\$1) near5 (unpredict\$5 or variable or different)	3793	<u>L4</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L3</u>	L2 and bandwidth	1	<u>L3</u>
<u>L2</u>	L1 and header\$1	1	<u>L2</u>
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## » Key

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IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

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- ☐ 2. **A survey of parallel computer architectures**  
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